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U.S. PATENT DOCUMENTS							
Examiner Initials	Cite	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	No.	Number- Kind Code (if known)					
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FOREIGN PATENT DOCUMENTS							
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Complete if Known Form 1449/PTO **Application Number** Not Yet Assigned Filing Date November 4, 2003 INFORMATION DISCLOSURE First Named Inventor K.B. Desai STATEMENT BY APPLICANT Art Unit Not Yet Assigned 2863 (use as many sheets as necessary) Examiner Name Not Yet Assigned Page 2 of Attorney Docket Number 2 P8966

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Т		
WH		COMER, D. J. "Chapter 6: Introduction to State Machines; Chapter 10: Asynchronous State Machines" In <i>Digital Logic and State Machine Design</i> . Saunders College Publishing: Philadelphia, etc., 2 rd Edition, 1990, pp. i-xii, 239-261, 455-475.			
WH		TAUB, H. and DONALD SCHILLING. "Preface, Chapter 3: Logic Circuits; and Chapter 6: Asynchronous State Machines," In <i>Digital Integrated Electronics</i> McGraw-Hill Electrical and Electronic Engineering Series. McGraw-Hill Book Co.: New York, 1977, 1st 5 pages unnumbered, ix-x, 8-133.			
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.								